

Claims

1. A switching arrangement for the controlled parallel switching of an at least $500 \mu\text{F}$ capacity electrolytic condenser to another, energy-storing electrolytic condenser; the
5 switching arrangement comprises an electronic switch formed by a semiconductor device and a delay member; the semiconductor device has a control input which is, through an RC-type delay member, connected to a control input (VS) supplying the switching signal; the switching arrangement has a switching delay of predetermined extent; characterized in that the semiconductor device is a field effect transistor (FT); the main circuit of the field effect
10 transistor (FT) is coupled through an inductive element (L), which supplies a second delay, to the condenser (C1) to be switched; the inductive element is a conductor (10) of determined length surrounded by a high-frequency ferrite core (11, 12); the delay effected by the RC member ensures only that fraction of the defined switching delay which is required for a load within the load limit of the field effect transistor (FT), and the remaining delay is supplied by
15 the inductive element (L).

2. The switching arrangement as defined in claim 1, characterized in that the ferrite core (11, 12) has two bores which are at a predetermined distance from one another and which have parallel axes; the conductor (10) has two legs passed through the bores.

3. The switching arrangement as defined in claim 2, characterized in that it
20 comprises a plurality of short, stacked ferrite cores (11).

4. The switching arrangement as defined in claim 1, characterized in that in the RC member the capacitive element is formed by the input capacity of the field effect transistor (FT) and the unavoidable scattered capacities.

5. The switching arrangement as defined in claim 1, characterized in that the
25 capacity of the switched electrolytic condensers is in the range of $10,000 \mu\text{F}$.